DAQ for helios

First Step :

connect DAQ Compute inside the exp hall :

open a terminal and input : sshdig

Password: gam\$hippie (directory: /home/dgs)

gotoacq

(directory:

/global/devel/systems/helios/h060)

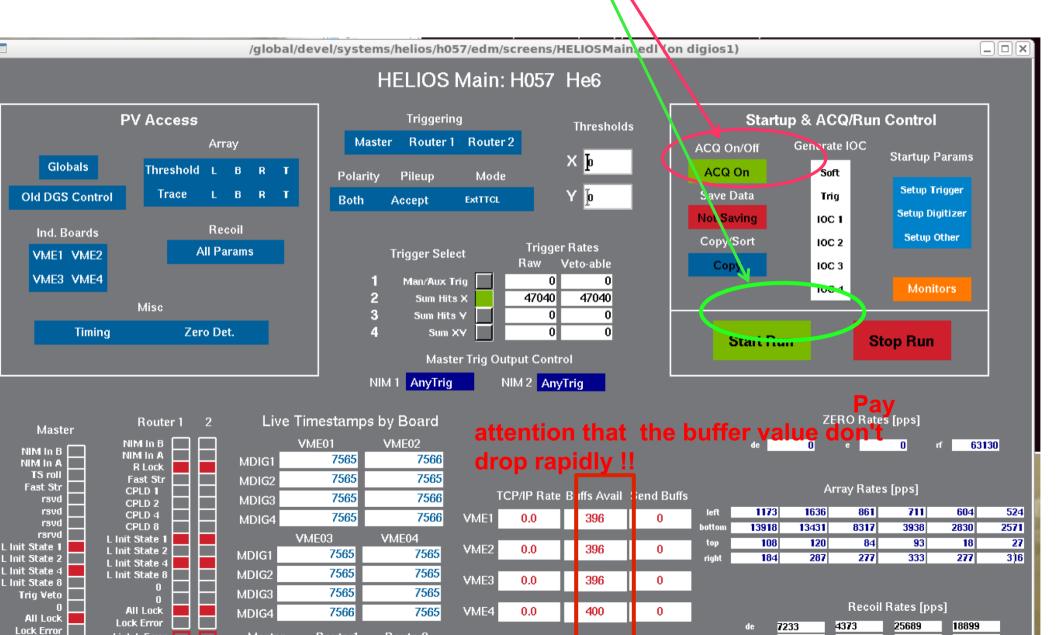
./heliosCommander*

Then enter the interface of DAQ

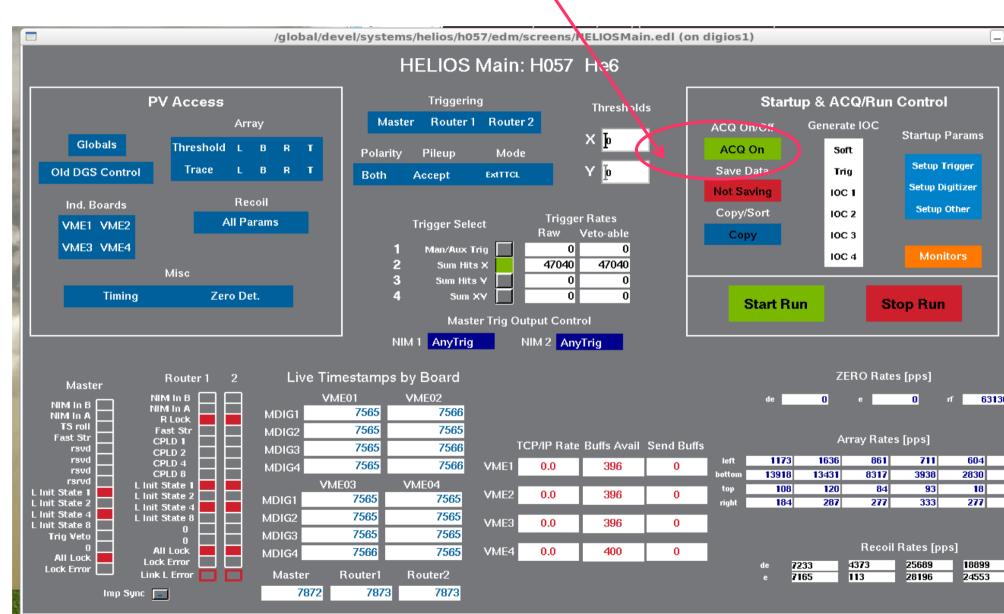
Interface of DAQ

Data not saved in ACQ mode

-Data, saved-in-Run-mode-



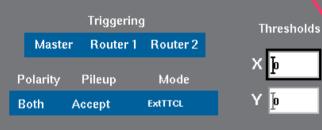
ACQ on



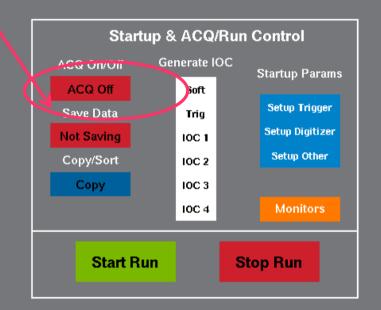
ACQ off

HELIOS Main: H057 He6

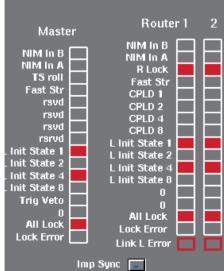
PV Access Array Globals Threshold L B R T Old DGS Control Trace L B R T Recoil Ind. Boards All Params VME1 VME2 VME3 VME4 Misc Zero Det. Timing



	Frigger Select		Tr Rav		er Rates Veto-able			
1	Man/Aux Trig			0	0			
2	Sum Hits X			0	0			
3	Sum Hits ¥			0	0			
4	Sum X¥			0	0			
Master Trig Output Control								
NIM	1 AnyTrig		NIM 2	Any	yTrig			









	VME01	VME02
MDIG1	9667	9668
MDIG2	9669	9668
MDIG3	9667	9668
MDIG4	9669	9668
	VME03	VME04
MDIG1	9668	9668
MDIG2	9668	9668
MDIG3	9668	9668
MDIG4	9668	9668
Maste	er Router1	Router2
5	511 551	2 5513

٧M

VME3

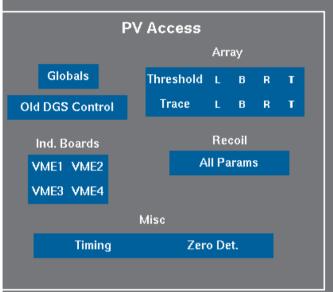
VME4

	TCP/IP Rate	Buffs Avail	Send Buffs	
IE1	0.0	400	0	
IE2	0.0	400	0	

VM



Start Run(1)



HELIOS Main: H057 He6



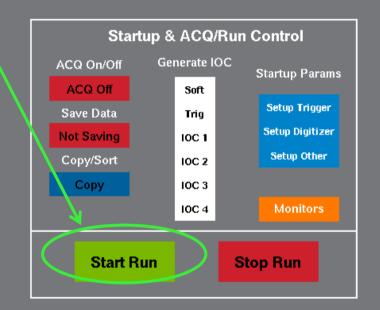
	Trigger Select		Tri Rav		r Rates Veto-abli	•		
1	Man/Aux Trig		nav	0		e 0		
2	Sum Hits X			0		0		
3	Sum Hits ¥			0		0		
4	Sum X¥			0	(0		
Master Trig Output Control								
NIN	11 AnyTrig		NIM 2	Any	/Trig			

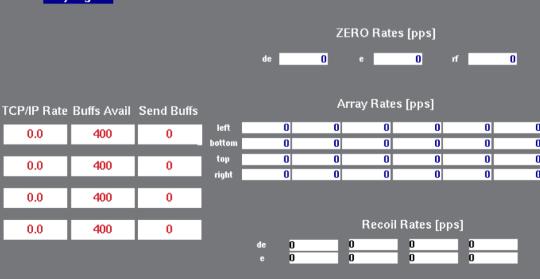
VME1

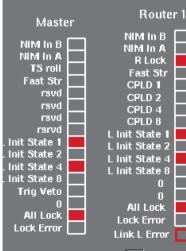
VME2

VME3

VME4









Live Timestamps by Board

	VME01	VME02
MDIG1	9667	9668
MDIG2	9669	9668
MDIG3	9667	9668
MDIG4	9669	9668
	VME03	VME04
MDIG1	9668	9668
MDIG2	9668	9668
MDIG2 MDIG3	9668 9668	9668 9668
MDIG3	9668 9668	9668

0.0

0.0

0.0

0.0

400

400

400

400

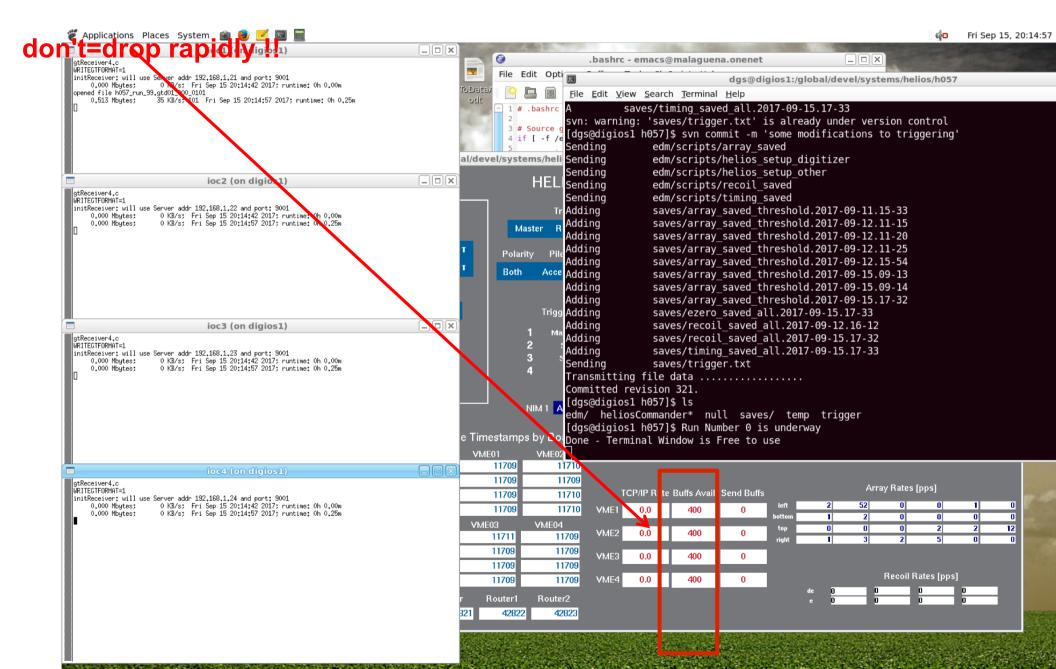
Start Run(2)

(no repeated)

				savedata (on digiosi	1)		
High magnetic field	ter a Run Number:	99					
2.50 T							
IELIOS_notice_ sign.pdf							
P\							
P1							
Globals							
Old DGS Control							
Ind. Boards							
VME1 VME2							
VME3 VME4							
Timing							
Master							
NIM IN B NIM IN B		VME01 VME02 11647 11648			de	0 e 0 ri	0
NIM In B NIM In B NIM In A R Lock TS roll Fast Str	MDIG1	VME01 VME02 11647 11648 11647 11647			de e		0
NIM In B NIM In A TS roll Fast Str rsvd CPLD 2	MDIG1 MDIG2 MDIG3	11647 11648 11647 11647 11647 11648		Buffs Avail Send Buffs		Array Rates [pps]	
NIM In B NIM In A TS roll Fast Str rsvd rsvd rsvd CPLD 4 CPLD 4	MDIG1 MDIG2 MDIG3 MDIG4	11647 11648 11647 11647 11647 11648 11647 11648 11647 11648		Buffs Avail Send Buffs 400 0	de entre de bottom	Array Rates [pps]	
NIM In B NIM In A TS roll rsvd rsvd rsvd t State 1 Linit State 1	MDIG1 MDIG2 MDIG3 MDIG4	11647 11648 11647 11647 11647 11648 11647 11648 11647 11648 VME03 VME04			left 0 bottom 0 top 0	Array Rates [pps]	
NIM In B NIM In A TS roll rsvd rsvd rsvd t State 1 t State 4 t State 4 t State 4 t State 4 t State 1 t State 4 t State 4 t State 4 t State 5 t State 2 t State 4 t State 5 t State 2 t State 3 t Sta	MDIG1 MDIG2 MDIG3 MDIG4 MDIG4 MDIG1 MDIG2	11647 11648 11647 11647 11647 11648 11647 11648 11647 11648	VME1 0.0 VME2 0.0	400 0 400 0	left 0 bottom 0	Array Rates [pps]	
NIM In B NIM In B NIM In A R Lock TS roll Fast Str rsvd CPLD 1 rsvd CPLD 2 rsvd CPLD 8 rsvd Linit State 1 t State 1 Linit State 4 t State 4 Linit State 4 t State 8 0 rig Veto 0	MDIG1 MDIG2 MDIG3 MDIG4 MDIG4 MDIG1 MDIG2 MDIG2 MDIG3	11647 11648 11647 11647 11647 11648 11647 11648 VME03 VME04 11649 11647 11647 11647 11649 11647 11647 11647 11647 11647	VME1 0.0 VME2 0.0 VME3 0.0	400 0 400 0 400 0	left 0 bottom 0 top 0	Array Rates [pps] 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
NIM In B NIM In A TS roll rsvd rsvd rsvd CPLD 2 rsvd CPLD 2 rsvd CPLD 4 rsvd CPLD 8 rsvd CPLD 8 rsvd CPLD 8 rsvd CPLD 8 rsvd CPLD 8 rsvd CPLD 1 t State 1 t State 2 t State 2 t State 2 t State 3 t State 8 t State 9 t State 2 t State 3 t State 8 t State 9 t State 1 t State 1 t State 1 t State 7 t	MDIG1 MDIG2 MDIG3 MDIG4 MDIG1 MDIG1 MDIG2 MDIG3 MDIG3	11647 11648 11647 11647 11647 11648 11647 11648 VME03 VME04 11649 11647 11649 11647 11649 11647 11647 11647 11647 11647 11647 11647	VME1 0.0 VME2 0.0	400 0 400 0	left 0 bottom 0 top 0 right 0	Array Rates [pps] 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
NIM in B NIM in A TS roll Fast Str rsvd CPLD 2 rsvd CPLD 2 rsvd CPLD 4 rsvd CPLD 2 rsvd CPLD 4 t State 1 L Init State 2 t State 4 L Init State 6 t State 4 L Init State 6 0 All Lock	MDIG1 MDIG2 MDIG3 MDIG4 MDIG1 MDIG1 MDIG2 MDIG3 MDIG3	11647 11648 11647 11647 11647 11648 11647 11648 VME03 VME04 11649 11647 11649 11647 11649 11647 11647 11647 11648 11647 11647 11647 11648 Nouter1	VME1 0.0 VME2 0.0 VME3 0.0	400 0 400 0 400 0	left 0 bottom 0 top 0	Array Rates [pps] 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	

Start Run(3)

Pay attention that the buffer value



Should see all files opening after a few minutes

<pre>Edit View Search Terminal Help S@digios1 h057_he6]\$ ll -h h057_run_121.gtd0** xr-xr-x. 1 dgs dgs 955M Sep 16 08:41 h057_run_121.gtd01_000_0101* xr-xr-x. 1 dgs dgs 955M Sep 16 08:43 h057_run_121.gtd01_002_0101* xr-xr-x. 1 dgs dgs 955M Sep 16 08:45 h057_run_121.gtd01_003_0101* xr-xr-x. 1 dgs dgs 954M Sep 16 08:47 h057_run_121.gtd01_004_0101* xr-xr-x. 1 dgs dgs 954M Sep 16 08:51 h057_run_121.gtd01_005_0101* xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 h057_run_121.gtd02_000_0105* xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 h057_run_121.gtd02_000_0107* s@digios1 h057_he6]\$</pre>	
<pre>xr-xr-x. 1 dgs dgs 955M Sep 16 08:41 h057_run_121.gtd01_000_0101* xr-xr-x. 1 dgs dgs 954M Sep 16 08:43 h057_run_121.gtd01_001_0101* xr-xr-x. 1 dgs dgs 955M Sep 16 08:45 h057_run_121.gtd01_002_0101* xr-xr-x. 1 dgs dgs 955M Sep 16 08:47 h057_run_121.gtd01_003_0101* xr-xr-x. 1 dgs dgs 954M Sep 16 08:48 h057_run_121.gtd01_004_0101* xr-xr-x. 1 dgs dgs 611M Sep 16 08:51 h057_run_121.gtd01_005_0101* xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 h057_run_121.gtd02_000_0105* xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 h057_run_121.gtd02_000_0105*</pre>	
xr-xr-x. 1 dgs dgs 954M Sep 16 08:43 h057_run_121.gtd01_001_0101* xr-xr-x. 1 dgs dgs 955M Sep 16 08:45 h057_run_121.gtd01_002_0101* xr-xr-x. 1 dgs dgs 955M Sep 16 08:47 h057_run_121.gtd01_003_0101* xr-xr-x. 1 dgs dgs 954M Sep 16 08:48 h057_run_121.gtd01_004_0101* xr-xr-x. 1 dgs dgs 611M Sep 16 08:51 h057_run_121.gtd01_005_0101* xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 h057_run_121.gtd02_000_0105* xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 h057_run_121.gtd02_000_0105*	
xr-xr-x. 1 dgs dgs 955M Sep 16 08:45 h057_run_121.gtd01_002_0101* xr-xr-x. 1 dgs dgs 955M Sep 16 08:47 h057_run_121.gtd01_003_0101* xr-xr-x. 1 dgs dgs 954M Sep 16 08:48 h057_run_121.gtd01_004_0101* xr-xr-x. 1 dgs dgs 611M Sep 16 08:51 h057_run_121.gtd01_005_0101* xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 h057_run_121.gtd02_000_0105* xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 h057_run_121.gtd02_000_0107*	
xr-xr-x. 1 dgs dgs 955M Sep 16 08:47 h057_run_121.gtd01_003_0101* xr-xr-x. 1 dgs dgs 954M Sep 16 08:48 h057_run_121.gtd01_004_0101* xr-xr-x. 1 dgs dgs 611M Sep 16 08:51 h057_run_121.gtd01_005_0101* xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 h057_run_121.gtd02_000_0105* xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 h057_run_121.gtd02_000_0107*	
xr-xr-x. 1 dgs dgs 954M Sep 16 08:48 h057_run_121.gtd01_004_0101* xr-xr-x. 1 dgs dgs 611M Sep 16 08:51 h057_run_121.gtd01_005_0101* xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 h057_run_121.gtd02_000_0105* xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 h057_run_121.gtd02_000_0107*	
xr-xr-x. 1 dgs dgs 611M Sep 16 08:51 <mark>h057_run_121.gtd01_005_0101*</mark> xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 <mark>h057_run_121.gtd02_000_0105*</mark> xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 <mark>h057_run_121.gtd02_000_0107</mark> *	
xr-xr-x. 1 dgs dgs 2.6M Sep 16 08:49 <mark>h057_run_121.gtd02_000_0105*</mark> xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 <mark>h057_run_121.gtd02_000_0107</mark> *	
xr-xr-x. 1 dgs dgs 59M Sep 16 08:51 h057_run_121.gtd02_000_0107*	
s@digios1 h057_he6]\$	